
CHAPTER	MARIE: An Introduction to a Simple Computer	145
4		
4.1	Introduction 145	
4.1.1	CPU Basics and Organization 145	
4.1.2	The Bus 147	
4.1.3	Clocks 151	
4.1.4	The Input/Output Subsystem 153	
4.1.5	Memory Organization and Addressing 153	
4.1.6	Interrupts 156	
4.2	MARIE 157	
4.2.1	The Architecture 157	
4.2.2	Registers and Buses 159	
4.2.3	The Instruction Set Architecture 160	
4.2.4	Register Transfer Notation 163	
4.3	Instruction Processing 166	
4.3.1	The Fetch-Decode-Execute Cycle 166	
4.3.2	Interrupts and I/O 166	
4.4	A Simple Program 169	
4.5	A Discussion on Assemblers 170	
4.5.1	What Do Assemblers Do? 170	
4.5.2	Why Use Assembly Language? 173	
4.6	Extending Our Instruction Set 174	
4.7	A Discussion on Decoding: Hardwired vs. Microprogrammed Control 179	
4.8	Real-World Examples of Computer Architectures 182	
4.8.1	Intel Architectures 183	
4.8.2	MIPS Architectures 187	
	Chapter Summary 189	
	Further Reading 190	
	References 191	
	Review of Essential Terms and Concepts 192	
	Exercises 193	

CHAPTER	A Closer Look at Instruction Set Architectures	199
5		
5.1	Introduction 199	
5.2	Instruction Formats 199	
5.2.1	Design Decisions for Instruction Sets 200	

- 5.2.2 Little versus Big Endian 201
- 5.2.3 Internal Storage in the CPU: Stacks versus Registers 203
- 5.2.4 Number of Operands and Instruction Length 204
- 5.2.5 Expanding Opcodes 208
- 5.3 Instruction Types 210**
- 5.4 Addressing 211**
 - 5.4.1 Data Types 211
 - 5.4.2 Address Modes 212
- 5.5 Instruction-Level Pipelining 214**
- 5.6 Real-World Examples of ISAs 219**
 - 5.6.1 Intel 220
 - 5.6.2 MIPS 220
 - 5.6.3 Java Virtual Machine 221
- Chapter Summary 225
- Further Reading 226
- References 227
- Review of Essential Terms and Concepts 228
- Exercises 229

**CHAPTER
6**

Memory

233

- 6.1 Memory 233**
- 6.2 Types of Memory 233**
- 6.3 The Memory Hierarchy 235**
 - 6.3.1 Locality of Reference 237
- 6.4 Cache Memory 237**
 - 6.4.1 Cache Mapping Schemes 239
 - 6.4.2 Replacement Policies 247
 - 6.4.3 Effective Access Time and Hit Ratio 248
 - 6.4.4 When Does Caching Break Down? 249
 - 6.4.5 Cache Write Policies 249
- 6.5 Virtual Memory 250**
 - 6.5.1 Paging 251
 - 6.5.2 Effective Access Time Using Paging 258
 - 6.5.3 Putting It All Together: Using Cache, TLBs, and Paging 259
 - 6.5.4 Advantages and Disadvantages of Paging and Virtual Memory 259
 - 6.5.5 Segmentation 262
 - 6.5.6 Paging Combined with Segmentation 263

6.6 A Real-World Example of Memory Management 263

- Chapter Summary 264
- Further Reading 265
- References 266
- Review of Essential Terms and Concepts 266
- Exercises 267

CHAPTER 7 Input/Output and Storage Systems 273

- 7.1 Introduction 273**
- 7.2 Amdahl's Law 274**
- 7.3 I/O Architectures 275**
 - 7.3.1 I/O Control Methods 276
 - 7.3.2 I/O Bus Operation 280
 - 7.3.3 Another Look at Interrupt-Driven I/O 283
- 7.4 Magnetic Disk Technology 286**
 - 7.4.1 Rigid Disk Drives 288
 - 7.4.2 Flexible (Floppy) Disks 292
- 7.5 Optical Disks 293**
 - 7.5.1 CD-ROM 294
 - 7.5.2 DVD 297
 - 7.5.3 Optical Disk Recording Methods 298
- 7.6 Magnetic Tape 299**
- 7.7 RAID 301**
 - 7.7.1 RAID Level 0 302
 - 7.7.2 RAID Level 1 303
 - 7.7.3 RAID Level 2 303
 - 7.7.4 RAID Level 3 304
 - 7.7.5 RAID Level 4 305
 - 7.7.6 RAID Level 5 306
 - 7.7.7 RAID Level 6 307
 - 7.7.8 Hybrid RAID Systems 308
- 7.8 Data Compression 309**
 - 7.8.1 Statistical Coding 311
 - 7.8.2 Ziv-Lempel (LZ) Dictionary Systems 318
 - 7.8.3 GIF Compression 322
 - 7.8.4 JPEG Compression 323

Chapter Summary 328
 Further Reading 328
 References 329
 Review of Essential Terms and Concepts 330
 Exercises 332
 Focus on Selected Disk Storage Implementations 335
 7A.1 Introduction 335
 7A.2 Data Transmission Modes 335
 7A.3 SCSI 338
 7A.4 Storage Area Networks 350
 7A.5 Other I/O Connections 352
 7A.6 Summary 354
 Exercises 354

**CHAPTER
8**

System Software

357

8.1 Introduction 357
8.2 Operating Systems 358
 8.2.1 Operating Systems History 359
 8.2.2 Operating System Design 364
 8.2.3 Operating System Services 366
8.3 Protected Environments 370
 8.3.1 Virtual Machines 371
 8.3.2 Subsystems and Partitions 374
 8.3.3 Protected Environments and the Evolution of Systems Architectures 376
8.4 Programming Tools 378
 8.4.1 Assemblers and Assembly 378
 8.4.2 Link Editors 381
 8.4.3 Dynamic Link Libraries 382
 8.4.4 Compilers 384
 8.4.5 Interpreters 388
8.5 Java: All of the Above 389
8.6 Database Software 395
8.7 Transaction Managers 401
 Chapter Summary 403
 Further Reading 404